

DeCAIR Course Syllabus Form

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Organization Name(s)	The University of Jordan		
WP Number & Title	Work Package 6: Improving Existing B.Sc. Programs in Jordan and Lebanon by Implementing or Including AI and Robotics Courses		
Activity Number & Title	Task 6.1: Developing syllabi and content for added/modified courses in existing BSc programs in universities of partner countries		
WP Leader	Jorge Casillas, University of Granada		
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Revision History

Version	Date	Author	Description	Action *	Page(s)
1	8/11/2021	Adham Alsharkawi	Original (base) document	C	1-5
2	18/12/2021	Adham Alsharkawi	Original (Base) document	U	1-5
3					
4					

(*) Action: C = Creation, I = Insert, U = Update, R = Replace, D = Delete

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Course title	Digital Control																			
Course number	0908483																			
Credit hours (lecture and lab)	3 (3 + 0)																			
ECTS (weekly contact and self-study load)	6 (3 + 3)																			
Prerequisites/co-requisites	0908382 (Control Systems), 0908371 (Engineering Measurements and Signal Processing)																			
Prerequisites by topic	Basics of analog control																			
Level and type (compulsory, elective)	BSc obligatory course																			
Year of study and semester	Fourth year, first semester																			
Description	This course introduces the fundamental concepts, principles and applications of digital control system analysis and design to undergraduate students. The course begins with an introduction to digital control and the reasons for its popularity. It then proceeds to consider discrete-time models and their analysis using the z-transform. Simple mathematical models for linear discrete-time systems are also derived in this course. Then, the course moves on to present stability tests for input-output systems. The course ends with the topic digital control design. This topic begins with proportional control design then examines digital controllers based on analog design. The direct design of digital controllers is considered next.																			
Objectives	<ol style="list-style-type: none"> 1. Introduce students to the importance of digital control. 2. Introduce students to discrete-time systems. 3. Introduce students to modeling of digital control systems. 4. Introduce students to stability of digital control systems. 5. Introduce students to digital control system design. 																			
Intended learning outcomes	<p>Upon successful completion of this course, students will be able to:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">No</th> <th style="width: 70%;">Intended learning Outcome (ILO)</th> <th style="width: 20%;">Program learning outcome (PLO)*</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Acquire the basic knowledge of digital control</td> <td>1</td> </tr> <tr> <td>2</td> <td>Demonstrate knowledge of discrete-time systems</td> <td>2</td> </tr> <tr> <td>3</td> <td>Develop models of digital control systems</td> <td>1</td> </tr> <tr> <td>4</td> <td>Analyze the stability of digital control systems</td> <td>3</td> </tr> <tr> <td>5</td> <td>Assess digital control system design</td> <td>4</td> </tr> </tbody> </table> <p>(*) The PLOs are listed in the appendix</p>		No	Intended learning Outcome (ILO)	Program learning outcome (PLO)*	1	Acquire the basic knowledge of digital control	1	2	Demonstrate knowledge of discrete-time systems	2	3	Develop models of digital control systems	1	4	Analyze the stability of digital control systems	3	5	Assess digital control system design	4
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Teaching and learning methods	Development of ILOs is promoted through the following teaching and learning methods:																			

	<ul style="list-style-type: none"> • Lectures will be delivered face to face and through Microsoft Teams and will be recorded for later access. • The control lab is open for the students to practice the practical aspects. • The student attends the class presentations and participates in the class discussions. • The student joins the related online team/group and participates in its discussions. • The student studies the reference material, including books and videos. • The student solves the control assignments using appropriate tools. • The student carries out a term project for solving a particular control problem. • The student develops a professional report for the term report. • The student presents the term project in class. 																																																																												
Learning material	Textbook, class handouts, lecture notes, selected YouTube videos and recordings.																																																																												
Resources and references	<p>A- Required book(s), assigned reading and audio-visuals:</p> <ol style="list-style-type: none"> 1. M. Sami Fadali, Antonio Visioli. <i>Digital control engineering: analysis and design</i>. 3rd Edition. 2019. <p>B- Recommended book(s), material and media:</p> <ol style="list-style-type: none"> 1. Charles L. Phillips, H Troy Nagle, Aranya Chakraborty. <i>Digital Control System Analysis & Design</i>. 4th Edition. 2014. 																																																																												
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		Effect of the sampler on the transfer function of a cascade																										
		DAC, analog subsystem, and ADC combination transfer function																										
		Systems with transport lag																										
		The closed-loop transfer function																										
		Analog disturbances in a digital system																										
		Steady-state error and error constants																										
11-12		Stability of Digital Control Systems	4	A-1, B-1																								
		Definitions of stability																										
		Stable z-domain pole locations																										
		Stability conditions																										
		Stability determination																										
		Jury test																										
		Nyquist criterion																										
13-15		Digital Control System Design	5	A-1, B-1																								
		z-Domain root locus																										
		z-Domain digital control system design																										
		Digital implementation of analog controller design																										
		Direct z-domain digital controller design																										
		Frequency response design																										
		Direct control design																										
		Finite settling time design																										
Evaluation tools	<p>Opportunities to demonstrate achievement of the ILOs are provided through the following assessment tools:</p> <table border="1"> <thead> <tr> <th>Assessment tool</th> <th>Mark</th> <th>Topic(s)</th> <th>Time</th> </tr> </thead> <tbody> <tr> <td>Assignments</td> <td>5%</td> <td>Stability of Digital Control Systems</td> <td>W12</td> </tr> <tr> <td>Midterm exam</td> <td>30%</td> <td>Introduction to Digital Control, Discrete-Time Systems, Modelling of Digital Control Systems</td> <td>W8</td> </tr> <tr> <td>Term project</td> <td>15%</td> <td>Digital Control System Design</td> <td>W15</td> </tr> <tr> <td>Final exam</td> <td>50%</td> <td>All Topics</td> <td>W16</td> </tr> <tr> <td>Total</td> <td>100%</td> <td></td> <td></td> </tr> </tbody> </table>				Assessment tool	Mark	Topic(s)	Time	Assignments	5%	Stability of Digital Control Systems	W12	Midterm exam	30%	Introduction to Digital Control, Discrete-Time Systems, Modelling of Digital Control Systems	W8	Term project	15%	Digital Control System Design	W15	Final exam	50%	All Topics	W16	Total	100%		
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Student requirements	Students should have access to a computer and internet connection.																											
Course policies	A- Attendance policies:																											

	<ul style="list-style-type: none"> • Attendance is required. Class attendance will be taken every class and the university polices will be enforced in this regard. <p>B- Absences from exams and submitting assignments on time:</p> <ul style="list-style-type: none"> • A makeup exam can be arranged for students with acceptable absence causes. • Assignments submitted late, but before announcing or discussing the solution can be accepted with 25% penalty. • The project report must be handed in in time. <p>C- Health and safety procedures:</p> <ul style="list-style-type: none"> • All health and safety procedures of the university and the school should be followed. <p>D- Honesty policy regarding cheating, plagiarism, misbehavior:</p> <ul style="list-style-type: none"> • Open-book exams • All submitted work must be of the submitting student. • Other text or code must be properly quoted with clear source specification. • Cheating will not be tolerated. <p>E- Available university services that support achievement in the course:</p> <ul style="list-style-type: none"> • Microsoft Teams team and Moodle course page • AI Lab for practicing the practical aspects and solving the programming assignments. • Program announcements Facebook group
<p>Additional information</p>	<p>None</p>

Appendix

Learning Outcomes for the BSc in Mechatronics Engineering

Students who successfully complete the BSc in Mechatronics Engineering will be able to:

1. An ability to identify, formulate, and solve complex engineering problems by applying principles of engineering, science, and mathematics
2. An ability to apply engineering design to produce solutions that meet specified needs with consideration of public health, safety, and welfare, as well as global, cultural, social, environmental, and economic factors
3. An ability to communicate effectively with a range of audiences
4. An ability to recognize ethical and professional responsibilities in engineering situations and make informed judgments, which must consider the impact of engineering solutions in global, economic, environmental, and societal contexts
5. An ability to function effectively on a team whose members together provide leadership, create a collaborative and inclusive environment, establish goals, plan tasks, and meet objectives
6. An ability to develop and conduct appropriate experimentation, analyze and interpret data, and use engineering judgment to draw conclusions
7. An ability to acquire and apply new knowledge as needed, using appropriate learning strategies.